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Sheet 1 of 1 Atty. Docket No. Serial No. orm PTO-1449 U.S. DEPARTMENT OF COMMERCE 09/978.118 (REV. 7-80) PATENT AND TRADEMARK OFFICE US 010142 Applicant MONISHA GHOSH Filing Date Group 10/15/01 2681 INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) **U.S. PATENT DOCUMENTS** Document Date Name Class Sub-Filing Date Int. Number class If Approp. 0 11/25/97 ROSS 375 200 セクリカ AA AB AC AD Technology Center 2699 AF **FOREIGN PATENT DOCUMENTS** Country Class Sub-Trans. Number class Yes No AG АH ΑI OTHER (Including Author, Title, Date, Pertinent Pages, Etc.) Ghosh: "Adaptive chip-equalizers for synchronous DS=CDMA systems with pilot sequences" アフック GLOBECOM'01, 25-29 November 2001, pages 3385-3389 Hooli et al: "Performance Evaluation of Adaptive Chip-Level Channel Equalizers in WCDMA AK 27/1 Downlink" IEEE International Conference On Communications (ICC) 01, 11-14 June 2001, pages 1974-1979 Frank et al: "Adaptive Interference Suppression for Direct-Sequence CDMA Systems with Long AL. Spreading Codes" 36th Annual Allerton Conference On Communication, Control And Computing, 23-100 25 September 1998, pages 411-420 Komulainen et al: "Adaptive channel equalization and interference suppression for CDMA 1217 downlink" IEEE ISSSTA. IEEE International Symposium On Spread Spectrum Techniques And Applications, vol. 2, September 6, 2000, pages 363-367 Hooli et al: "Multiple Access Interference Supression With Linear Chip Equalizers In WCDMA #W/ AN Downlink Receivers" 1999 IEEE Global Telecommunications Conference, New York, NY: IEEE, US,

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